

Research status

My very initial work is to understand:

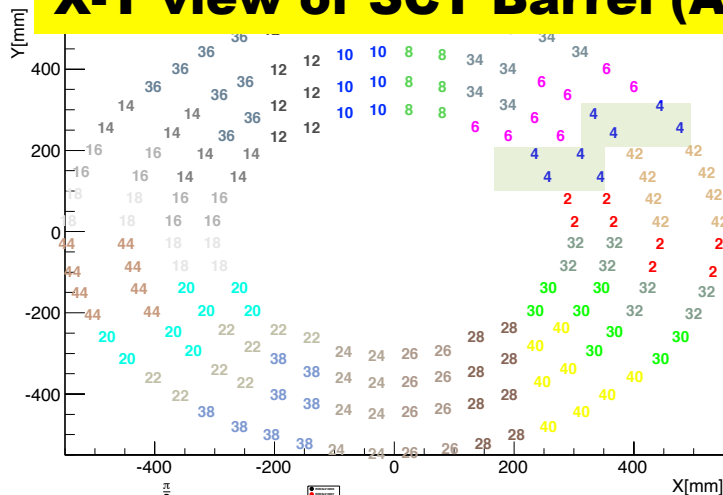
- Hardware mapping
 - Analyze the hardware mapping text database
 - Data format from ROD
 - Develop private decoding software
 - Document what I learn
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Hardware mapping

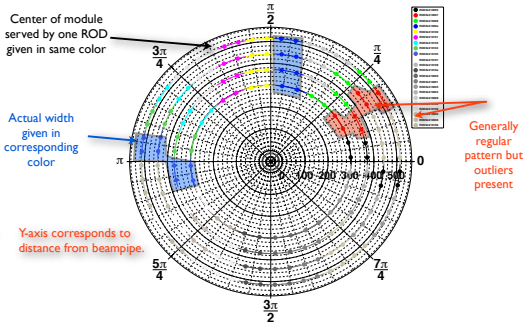
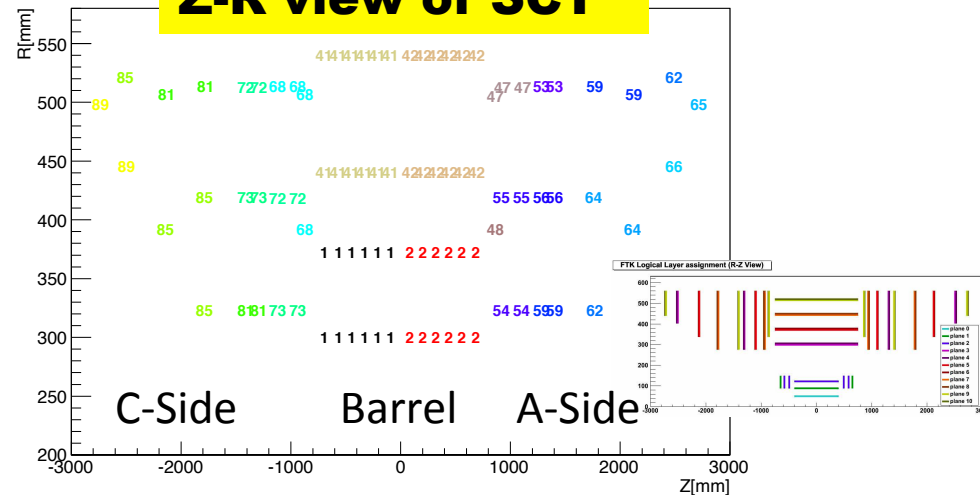
Mapping between FE modules & ROD

- Point : center of the FE modules (input of RODs)
- Number : ID of corresponding ROD

X-Y view of SCT Barrel (A)



Z-R view of SCT



← (From Bjeorn's talk) <https://email.fnal.gov/owa/redir.aspx?C=8c0952bed94a4247abb4f42d51d1eec6&URL=http%3a%2f%2fbt.ly%2fHEH2T1>

Links

Summaries:

- <http://twiki.mwt2.org/pub/Main/YasuyukiOkumura/yasuyuki.okumura.20120406.01.pdf>
- <http://twiki.mwt2.org/pub/Main/YasuyukiOkumura/yasuyuki.okumura.20120406.00.pdf>

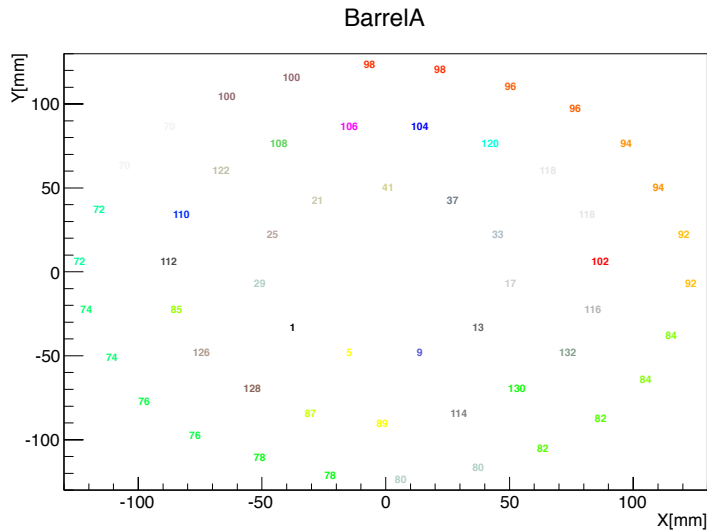
Software:

- Analysis code for mapping:
https://svnweb.cern.ch/trac/atlasusr/browser/okumura/ftk/myPackages/id_mapping
 - Mapping databases for: (1) Pixel & (2) SCT:
(1) https://svnweb.cern.ch/trac/atlasoff/browser/InnerDetector/InDetDetDescr/PixelCabling/trunk/share/Pixels_Atlas_IdMapping_May08.dat
(2) https://svnweb.cern.ch/trac/atlasoff/browser/InnerDetector/InDetDetDescr/SCT_Cabling/trunk/share/SCT_Sept08Cabling_svc.dat
 - Private decoder for raw data:
https://svnweb.cern.ch/trac/atlasusr/browser/okumura/ftk/myPackages/bytestream_checker
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Pixels

Pixel mapping between FE modules & ROD

X-Y view of Pixel Barrel (A)



Z-R view of Pixel

